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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Bollinger, et al.
SERIAL NO.: 10/719,829
FILING DATE: 20-Nov-2003
TITLE: Method For Picking Semiconductor Chips From A Foil
EXAMINER: Unassigned
ART UNIT: Unassigned

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STATUS INQUIRY LETTER

Please advise of the status for the above-identified patent application.

Respectfully submitted,
Thelen Reid & Priest LLP

Dated: 8/1, 2005

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